

Docket No. 200308565-1

Amendments to the Specification:

Please amend paragraphs [0072] and [0073] as indicated below:

[0072] Figure 10 illustrates another example memory quality assurance method 1000. The method 1000 may include, at 1010, selecting a first memory location to test from a first set of memory. The first set of memory may be, for example, in a main memory. The first memory location may be, for example, an individually addressable unit of memory like a byte, or a block, a page, and the like. The method 1000 may also include, at 1020, selectively copying the contents of the first memory location to a second memory location and, at 1030, logically replacing the first memory location with the second memory location. This leaves the first memory location logically isolated and thus available for non-intrusive testing (e.g., testing that will be transparent to an operating system that allocates and accesses memory). Thus, the method 1000 may also include, at 1040, initiating testing of the first memory location.

[0073] Figure 11 illustrates an example operating system transparent method 1100 for on-the-fly memory testing. The method 1100 may include, at 1110, identifying a test memory location and a mirroring memory location. The test memory location may be, for example, in a main memory and may be an individually addressable unit of memory like a byte, or a block, a page, and the like. The mirroring memory location may be in main memory, a set-aside buffer, a cache, a temporary memory, and so on. The method 1100 may also include, at 1120, mirroring the test memory location to the mirroring memory location. Thus, the contents of the test memory location are preserved in the mirroring memory location.